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(35 U.S.C. 122(b)(2)(B)(iii))**

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Application Number	10/613,246
Filing Date	July 3, 2003
First Named Inventor	Mario Au
Title	Integrated Circuit Memory Devices Having Clock Signal Arbitration Circuits Therein and Methods of Performing Clock Signals
Atty Docket Number	5646-42XX
Group Art Unit	To Be Assigned
Examiner	To Be Assigned

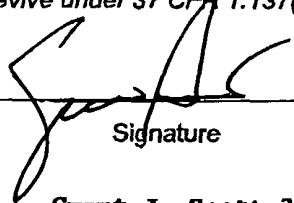
A request that the above-identified application not be published under 35 U.S.C. 122(b) (nonpublication request) was included with the above-identified application on filing pursuant to 35 U.S.C. 122(b)(2)(B)(i). I hereby rescind the previous nonpublication request.

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8/13/03

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